



DECLARATION

3

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **PROCESSING ARCHITECTURE HAVING PARALLEL ARITHMETIC CAPABILITY** the specification of which was filed on March 8, 2001 and assigned U.S. Patent Application Serial No. 09/801,564.

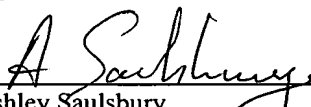
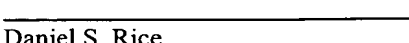
I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56. I claim foreign priority benefits under Title 35, United States Code, Section 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed.

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

Application No.	Filing Date
60/187,901	March 8, 2000

Full Name of Inventor 1:	Last Name: SAULSBURY	First Name: ASHLEY	Middle Name or Initial:
Residence & Citizenship:	City: Los Altos	State/Foreign Country: California	Country of Citizenship: United Kingdom
Post Office Address:	Post Office Address: 18448 Grizzly Rock Road	City: Los Altos	State/Country: California
			Postal Code: 95033
Full Name of Inventor 2:	Last Name: RICE	First Name: DANIEL	Middle Name or Initial: S.
Residence & Citizenship:	City: Oakland	State/Foreign Country: California	Country of Citizenship: United States
Post Office Address:	Post Office Address: 5838 Birch Court, #F	City: Oakland	State/Country: California
			Postal Code: 94618

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Signature of Inventor 1  Ashley Saulsbury	Signature of Inventor 2  Daniel S. Rice
Date 8/3/2001	Date



DECLARATION

As a below named inventor, I declare that:

My residence, post office address and citizenship are as stated below next to my name; I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **PROCESSING ARCHITECTURE HAVING PARALLEL ARITHMETIC CAPABILITY** the specification of which was filed on March 8, 2001 and assigned U.S. Patent Application Serial No. 09/801,564.

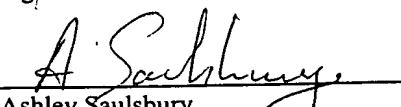
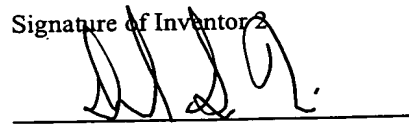
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I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

Application No.	Filing Date
60/187,901	March 8, 2000

Full Name of Inventor 1:	Last Name: SAULSBURY	First Name: ASHLEY	Middle Name or Initial:	
Residence & Citizenship:	City: Los Altos	State/Foreign Country: California	Country of Citizenship: United Kingdom	
Post Office Address:	Post Office Address: 18448 Grizzly Rock Road	City: Los Altos	State/Country: California	Postal Code: 95033
Full Name of Inventor 2:	Last Name: RICE	First Name: DANIEL	Middle Name or Initial: S.	
Residence & Citizenship:	City: Oakland	State/Foreign Country: California	Country of Citizenship: United States	
Post Office Address:	Post Office Address: 5838 Birch Court, #F	City: Oakland	State/Country: California	Postal Code: 94618

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Signature of Inventor 1  Ashley Saulsbury	Signature of Inventor 2  Daniel S. Rice
Date 8/3/2001	Date 8/24/2001

POWER OF ATTORNEY BY ASSIGNEE

SUN MICROSYSTEMS, INC., is the Assignee of the invention entitled PROCESSING ARCHITECTURE HAVING PARALLEL ARITHMETIC CAPABILITY, the specification of which was filed on March 8, 2001 as Application Serial No. 09/801,564.

The Assignment accompanying this Power of Attorney has been reviewed by the undersigned. The undersigned certifies that to the best of the undersigned's knowledge and belief, title is in the Assignee.

Assignee hereby appoints the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Kenneth Olsen, Reg. No. 26,493
Timothy J. Crean, Reg. No. 37, 116
Alexander E. Silverman, Reg. No. 37,940
Anirma R. Gupta, Reg. No. 38,275
Sean P. Lewis, Reg. No. 42,798
Michael J. Schallop, Reg. No. 44,319
Bernice B. Chen, Reg. No. 42,403
Noreen A. Krall, Reg. No. 39,734
Richard J. Lutton, Reg. No. 39,756
Monica D. Ward, Reg. No. 40,696
Marc D. Foodman, Reg. No. 34, 110
Naren Chaganti, Reg. No. 44,602
Elaine Lee, Reg. No. 41,936
Hugh H. Matsubayashi, Reg. No. 43,779
Paul D. Sorkin, Reg. No. 39,039
Marilyn E. Glaubenskle, Reg. No. 35,521
Ramin Aghevli, Reg. No. 43,462
Andrew C. Chen, Reg. No. 43,544
Jeffrey L. Myers, Reg. No. 44,252
of SUN MICROSYSTEMS, INC.
901 San Antonio Road, Palo Alto, CA 94303

and

Practitioners at Customer Number 20350
Townsend and Townsend and Crew, LLP

Send Correspondence to:

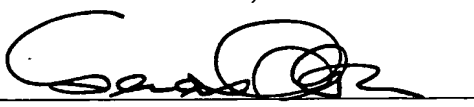
Thomas D. Franklin
TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, CA 94111-3834

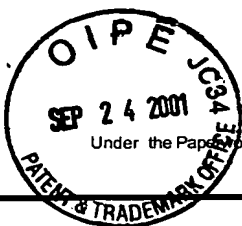
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Thomas D. Franklin, Reg. No. 43,616
303-571-4000
facsimile - 303-571-4321
e-mail - TDFranklin@Townsend.com

SUN MICROSYSTEMS, INC.

Date: July 12, 2001

By: 
Kenneth Olsen
Vice President, Intellectual Property

**STATEMENT UNDER 37 CFR 3.73(b)**Applicant/Patent Owner: Saulsbury, et al.Application No./Patent No.: 09/801,564Filed/Issue Date: March 8, 2001Entitled: PROCESSING ARCHITECTURE HAVING PARALLEL ARITHMETIC CAPABILITYSun Microsystems, Inc., a corporation

(Name of Assignee)

etc.)

(Type of Assignee, e.g., corporation, partnership, university, government agency,

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of an undivided part interest

in the patent application/patent identified above by virtue of either:

- A. ☒ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

- B. ☐ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

2. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

3. From: _____ To: _____
The document was recorded in the United States Patent and Trademark Office at
Reel _____, Frame _____, or for which a copy thereof is attached.

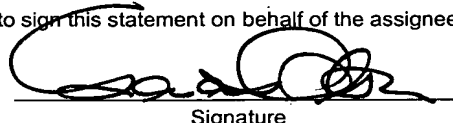
☐ Additional documents in the chain of title are listed on a supplemental sheet.

☐ Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

July 12, 2001
Date


Signature

Kenneth Olsen
Typed or printed name

Vice President, Intellectual Property
Title

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

DE 7042199 v1

ASSIGNMENT OF PATENT APPLICATION

JOINT

WHEREAS, Ashley Saulsbury, of 18488 Grizzly Rock Road, Los Altos, CA, 94022, and Daniel S. Rice of 5838 Birch Court #F, Oakland, CA, 94618, hereinafter referred to as "Assignors," are the inventors of the invention described and set forth in the below-identified application for United States Letters Patent:

Title of Invention: PROCESSING ARCHITECTURE HAVING PARALLEL ARITHMETIC CAPABILITY

Date(s) of execution of Declaration:

Filing Date: March 8, 2001

Application No.: 09/801,564; and

WHEREAS, Sun Microsystems, Inc., a corporation of the state of Delaware, located at 901 San Antonio Road, Palo Alto, CA, 94303, hereinafter referred to as "ASSIGNEE," is desirous of acquiring an interest in the invention and application and in any U.S. Letters Patent and Registrations which may be granted on the same;

For good and valuable consideration, receipt of which is hereby acknowledged by Assignors, Assignors have assigned, and by these presents do assign to Assignee all right, title and interest in and to the invention and application and to all foreign counterparts (including patent, utility model and industrial designs), and in and to any Letters Patent and Registrations which may hereafter be granted on the same in the United States and all countries throughout the world, and to claim the priority from the application as provided by the Paris Convention. The right, title and interest is to be held and enjoyed by Assignee and Assignee's successors and assigns as fully and exclusively as it would have been held and enjoyed by Assignors had this Assignment not been made, for the full term of any Letters Patent and Registrations which may be granted thereon, or of any division, renewal, continuation in whole or in part, substitution, conversion, reissue, prolongation or extension thereof.

Assignors further agree that they will, without charge to Assignee, but at Assignee's expense, (a) cooperate with Assignee in the prosecution of U.S. Patent applications and foreign counterparts on the invention and any improvements, (b) execute, verify, acknowledge and deliver all such further papers, including patent applications and instruments of transfer, and (c) perform such other acts as Assignee lawfully may request to obtain or maintain Letters Patent and Registrations for the invention and improvements in any and all countries, and to vest title thereto in Assignee, or Assignee's successors and assigns.


Assignors hereby authorize and request Townsend and Townsend and Crew LLP, Two Embarcadero Center, 8th Floor, San Francisco, CA 94111-3834, to insert herein above the application number and filing date of said application when known.

IN TESTIMONY WHEREOF, Assignors have signed their names on the dates indicated.

Dated: 8/3/2001


Ashley Saulsbury

Dated: 8/24/2001


Daniel S. Rice